# Requirements for the Xilinx ™ XUPv5

The following recommendation is based off a standard Xilinx™ XUPv5 without any expansion boards.

## Software/Tools

None

## Hardware

### General Requirements

ZigBee Connectivity (IEEE 802.15.4) over SPI

Bluetooth Connectivity over UART/RS232

16-bits of Test Headers (100 mil)

### XUPv5 Specific High Priority

Plexiglass Board Protection

FPGA Heatsink

NTSC In

8 7-Segment Displays

SDRAM

* The DDR2 controller is under evaluation from an educational perspective, and should be okay. Final results of this evaluation are pending.
* non-DDR may be required, though unlikely